

WHAT IS CLAIMED IS:

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1. A method for storing and retrieving digital data within a hardware platform, the method comprising:
- receiving data bits across a bus of a fixed width, the data bits forming a bit pattern;
- 5 altering the bit pattern of the data bits according to a prescribed scheme;
- storing the altered data bits;
- restoring the altered data bits to the bit pattern; and
- outputting the restored data bits.
2. The method according to claim 1, wherein the altering step comprises:
- 10 inverting a portion of the data bits on the fixed width bus, according to the prescribed scheme.
3. The method according to claim 1, wherein the altering step further comprises:
- selectively scrambling the data bits according to the prescribed scheme.
4. The method according to claim 1, wherein the altering step comprises:
- scrambling the data bits according to the prescribed scheme.
5. The method according to claim 1, wherein the altering step and the restoring step are performed by a hard disk drive interface.
6. The method according to claim 1, wherein the prescribed scheme in the step of
- 15 altering is unique to the hardware platform.
7. The method according to claim 1, wherein the prescribed scheme in the step of altering is relatively unique to the hardware platform in that the prescribed scheme may be utilized by another hardware platform.
8. The method according to claim 1, wherein the prescribed scheme in the step of altering is based upon a serial number of the hardware platform.
- 25 9. The method according to claim 1, further comprising:

generating a random number upon power-up of the hardware platform,

A7 wherein the prescribed scheme in the step of altering is based upon the random number.

10. The method according to claim 1, wherein the altered bits in the step of altering are stored in a hard disk drive.

11. The method according to claim 1, wherein the bus in the receiving step is a serial bus.

12. An apparatus for storing and retrieving digital video data, comprising:

A7 a system bus configured to transfer data bits of a fixed width, the data bits forming a bit pattern;

an interface coupled to system bus and configured to alter the bit pattern of the data bits according to a prescribed scheme; and

a hard disk drive coupled to the interface and configured to store the altered data bits.

13. The apparatus according to claim 12, wherein the interface is configured to invert a portion of the data bits according to the prescribed scheme.

14. The apparatus according to claim 12, wherein the interface is further configured to selectively scramble the data bits according to the prescribed scheme.

15. The apparatus according to claim 12, wherein the interface is configured to scramble positions of the data bits according to the prescribed scheme.

20 16. The apparatus according to claim 12, wherein the apparatus is a digital set-top box.

17. The apparatus according to claim 12, wherein the prescribed scheme is unique to the apparatus.

A7 18. The apparatus according to claim 12, wherein the prescribed scheme is relatively unique to the apparatus in that the prescribed scheme may be utilized by another apparatus.

19. The apparatus according to claim 12, wherein the prescribed scheme is based upon a serial number of the apparatus.

20. The apparatus according to claim 12, further comprising:  
a processor coupled to the system bus and configured to generate a random number during power-up of the apparatus,  
wherein the prescribed scheme is based upon the random number.

21. The apparatus according to claim 12, wherein the fixed width of the data bits is 16 bits.

22. The apparatus according to claim 12, wherein the system bus is a serial bus.

23. A system for storing and retrieving digital audio/video data, comprising:  
a satellite antenna configured to receive audio/video signals; and  
a set-top box coupled to the satellite antenna, the set-top box comprising,  
a receiver configured to output data bits corresponding to the received audio/video signals,  
a fixed width bus coupled to the receiver and configured to transfer data bits, the data bits forming a bit pattern,  
an interface coupled to the bus and configured to alter the bit pattern of the data bits according to a prescribed scheme, and  
a hard disk drive coupled to the interface and configured to store the altered data bits.

24. The system according to claim 23, wherein the interface is configured to invert a portion of the data bits according to the prescribed scheme.

25. The system according to claim 23, wherein the interface is further configured to selectively scramble the data bits according to the prescribed scheme.

26. The system according to claim 23, wherein the interface is configured to scramble the data bits according to the prescribed scheme.

27. The system according to claim 23, wherein the prescribed scheme is unique to the system.

28. The system according to claim 23, wherein the prescribed scheme is relatively unique to the system.

29. The system according to claim 23, wherein the prescribed scheme is based upon a serial number of the system.

30. The system according to claim 23, wherein the set-top box further comprises:  
a processor coupled to the system bus and configured to generate a random number during power-up of the system,

wherein the prescribed scheme is based upon the random number.

31. The system according to claim 23, wherein the fixed width of the data bits is 16 bits.

32. The system according to claim 23, wherein the bus is a serial bus.

A7 33. A computer-readable medium carrying one or more sequences of one or more instructions for storing and retrieving digital video data within a hardware platform, the one or more sequences of one or more instructions including instructions which, when executed by one or more processors, cause the one or more processors to perform the steps of:

receiving data bits across a bus of a fixed width, the data bits forming a bit pattern;

altering the bit pattern of the data bits according to a prescribed scheme;

storing the altered data bits;

restoring the altered data bits to the bit pattern; and

outputting the restored data bits.

34. The computer-readable medium according to claim 33, wherein the altering step comprises:

inverting a portion of the data bits on the fixed width bus, according to the prescribed scheme.

5 35. The computer-readable medium according to claim 33, wherein the altering step further comprises:

selectively scrambling positions of the data bits according to the prescribed scheme.

36. The computer-readable medium according to claim 33, wherein the altering step comprises:

scrambling positions of the data bits according to the prescribed scheme.

37. The computer-readable medium according to claim 33, wherein the altering step and the restoring step are performed by a hard disk drive interface.

38. The computer-readable medium according to claim 33, wherein the prescribed scheme in the step of altering is unique to the hardware platform.

39. The computer-readable medium according to claim 33, wherein the prescribed scheme in the step of altering is relatively unique to the hardware platform in that the prescribed scheme may be utilized by another hardware platform.

40. The computer-readable medium according to claim 33, wherein the prescribed scheme in the step of altering is based upon a serial number of the hardware platform.

20 41. The computer-readable medium according to claim 33, wherein the one or more processors further perform the step of:

generating a random number upon power-up of the hardware platform,

wherein the prescribed scheme in the step of altering is based upon the random number.

42. The computer-readable medium according to claim 33, wherein the altered bits in

A7 the step of altering are stored in a hard disk drive, the fixed width of the data bits being 16 bits.

43. The computer-readable medium according to claim 33, wherein the bus in the  
5 receiving step is a serial bus.

44. An apparatus for storing and retrieving digital video data within a hardware  
A7 platform, the apparatus comprising:

means for receiving data bits across of a bus of a fixed width, the data bits forming a  
bit pattern;

means for altering the bit pattern of the data bits according to a prescribed scheme;

means for storing the altered data bits;

means for restoring the altered data bits to the bit pattern; and

means for outputting the restored data bits.

45. The apparatus according to claim 44, wherein the means for altering comprises:

means for inverting a portion of the data bits according to the prescribed scheme.

46. The apparatus according to claim 44, wherein the means for altering further  
comprises:

means for selectively scrambling the data bits according to the prescribed scheme.

47. The apparatus according to claim 44, wherein the means for altering comprises:

20 means for scrambling the data bits according to the prescribed scheme.

48. The apparatus according to claim 44, wherein the prescribed scheme is unique to  
A7 the hardware platform.

49. The apparatus according to claim 44, wherein the prescribed scheme is relatively  
unique to the hardware platform in that the prescribed scheme may be utilized by another  
25 hardware platform.

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wherein the prescribed scheme is based upon the random number.

53. The apparatus according to claim 44, wherein the bus is a serial bus.

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